

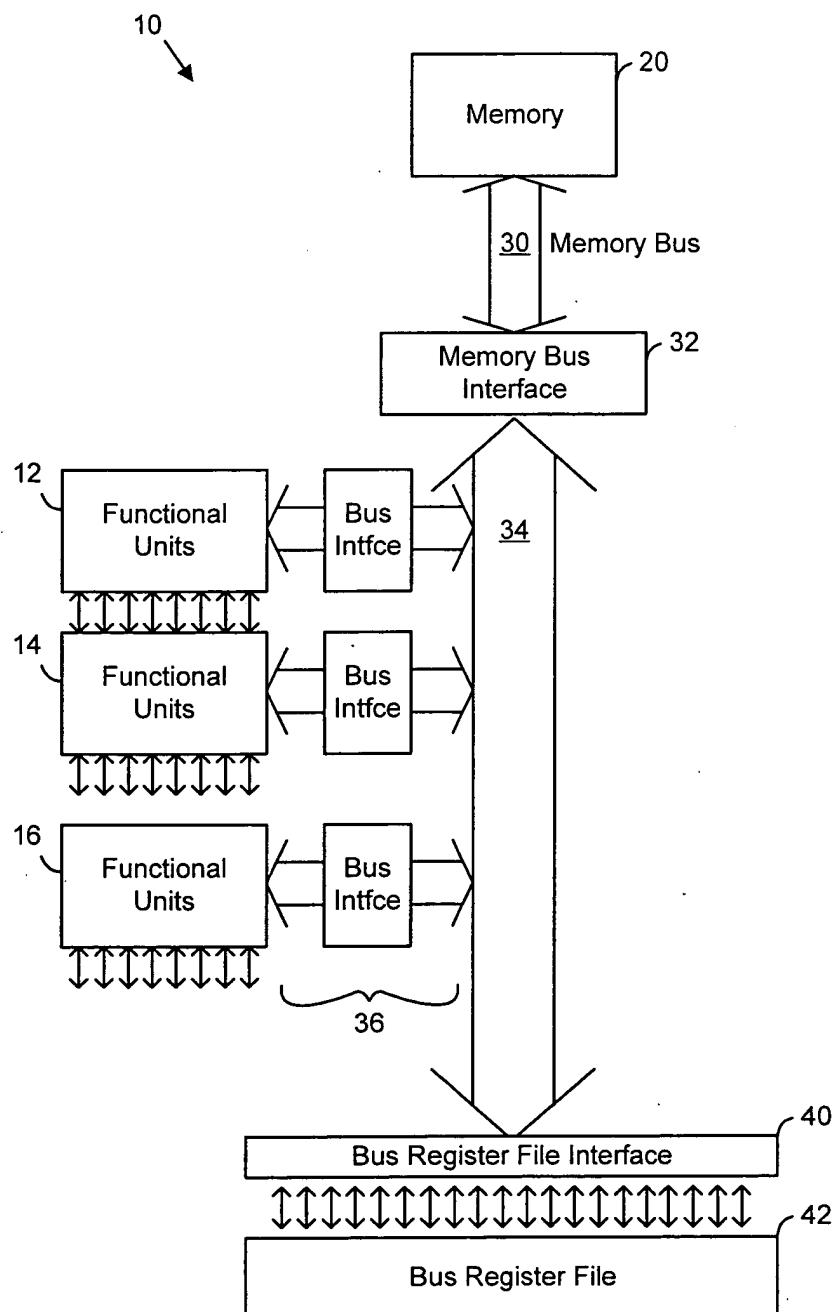
INPUT PIPELINE REGISTERS FOR A NODE IN AN

ADAPTIVE COMPUTING ENGINE

Inventors: Amit Ramchandran

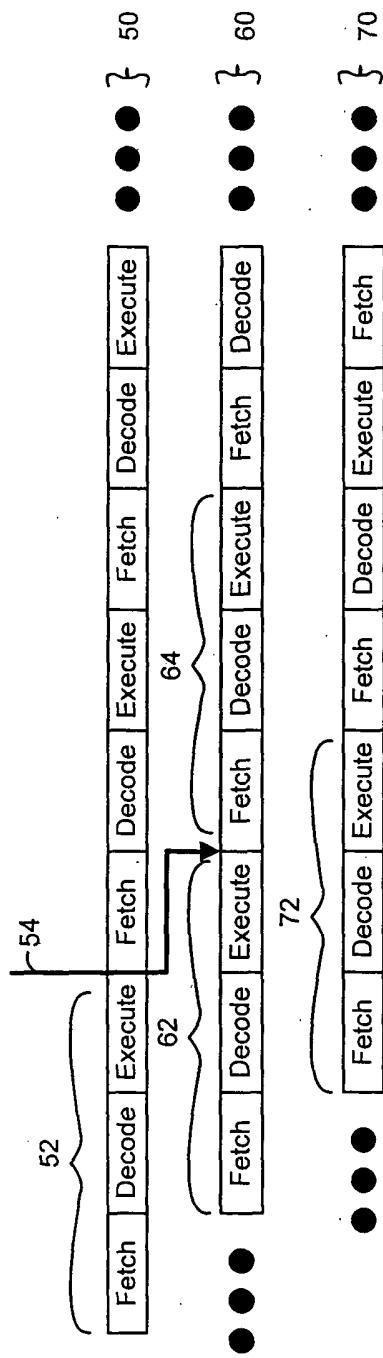
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PRIOR ART

Fig. 1A



PRIOR ART
Fig. 1B

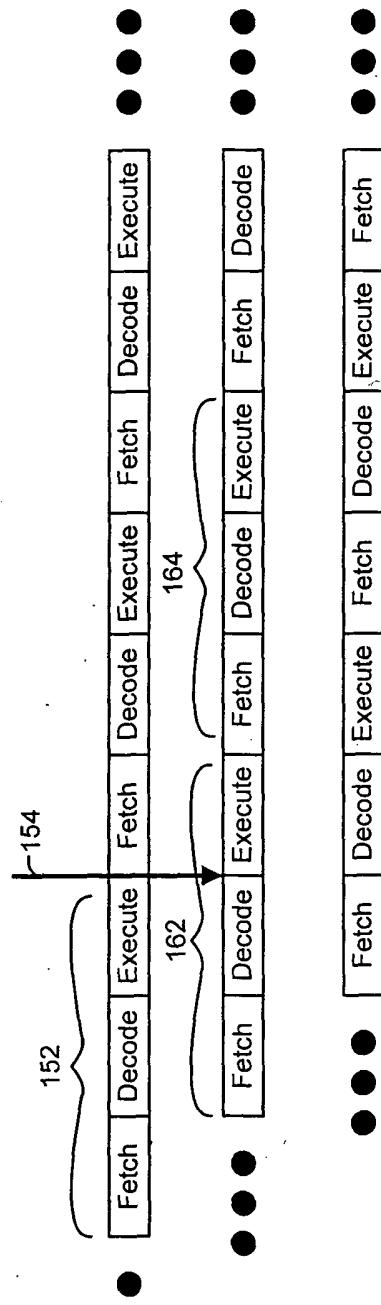


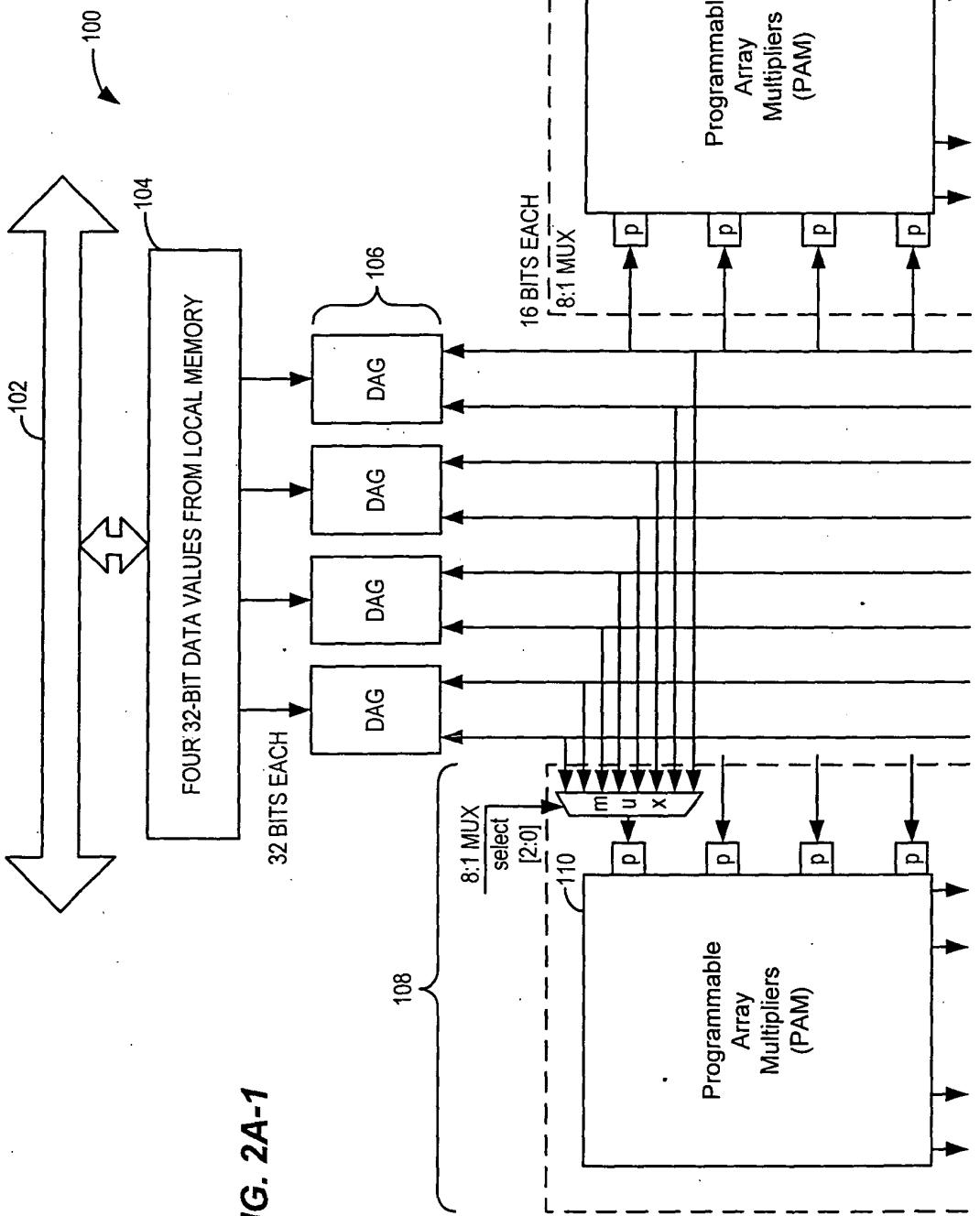
Fig. 2B

INPUT PIPELINE REGISTERS FOR A NODE IN AN
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FIG. 2A-1
FIG. 2A-2

FIG. 2A



INPUT PIPELINE REGISTERS FOR A NODE IN AN
ADAPTIVE COMPUTING ENGINE

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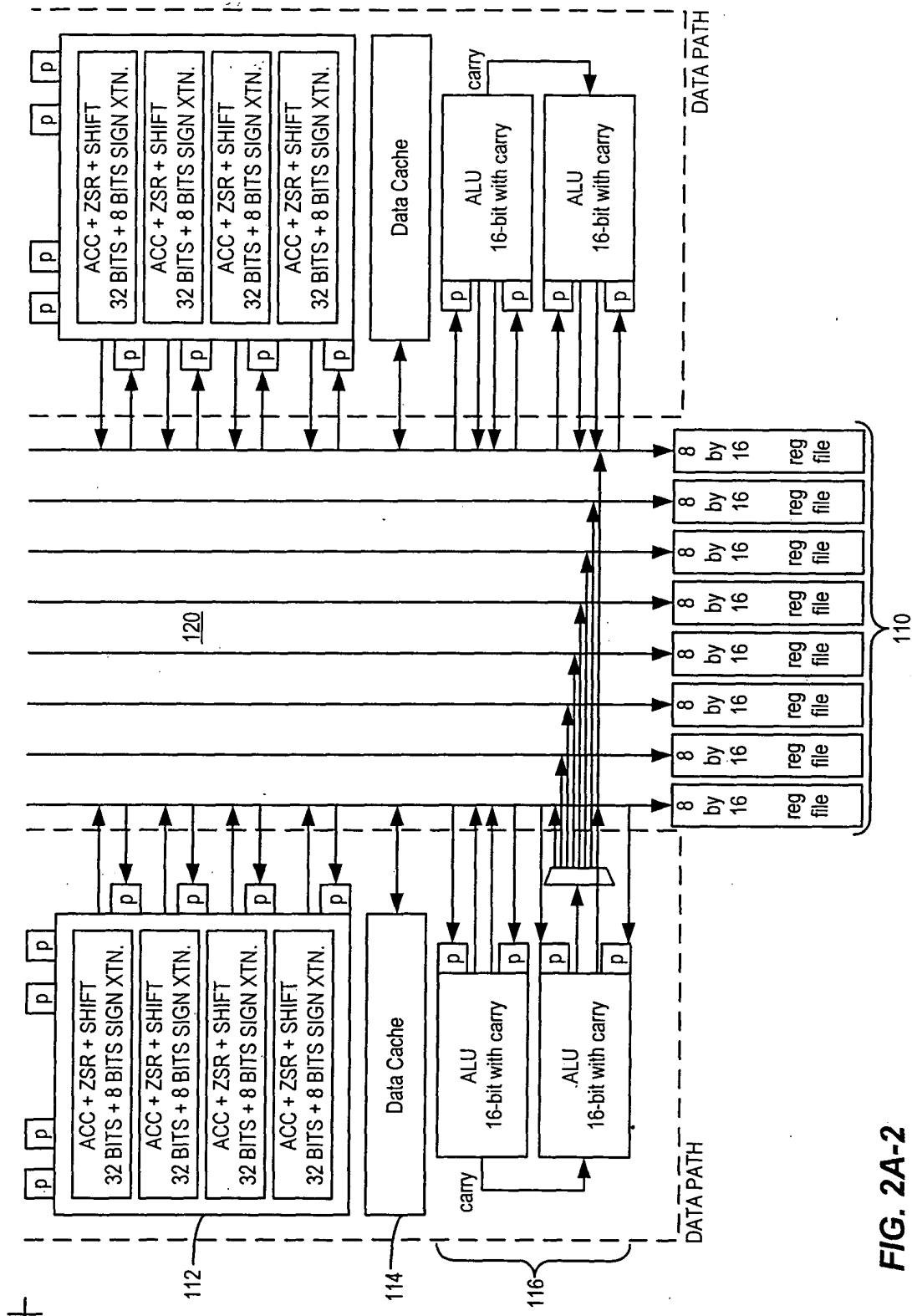
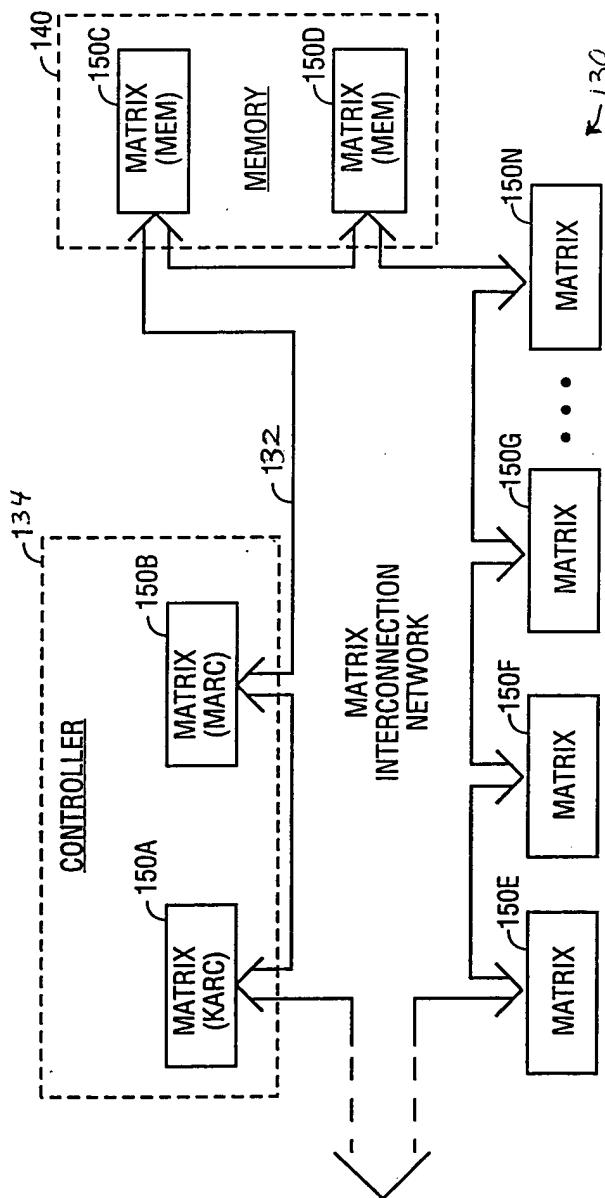


FIG. 2A-2

INPUT PIPELINE REGISTERS FOR A NODE IN AN
ADAPTIVE COMPUTING ENGINE

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ADAPTIVE COMPUTING ENGINE (ACE)

FIG. 3

INPUT PIPELINE REGISTERS FOR A NODE IN AN

ADAPTIVE COMPUTING ENGINE

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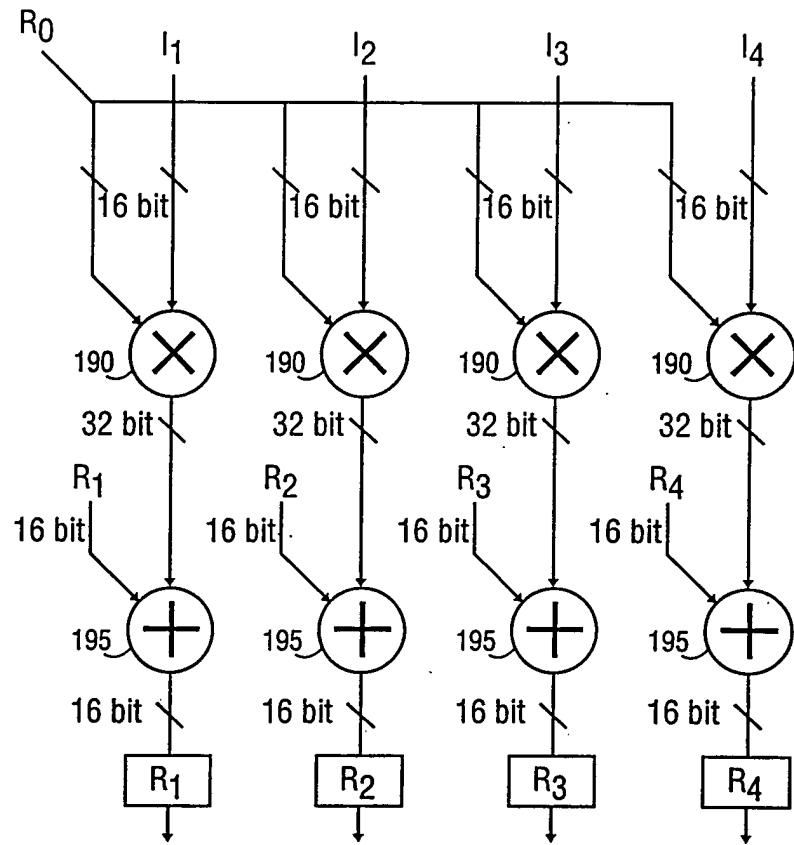


FIG. 4

INPUT PIPELINE REGISTERS FOR A NODE IN AN
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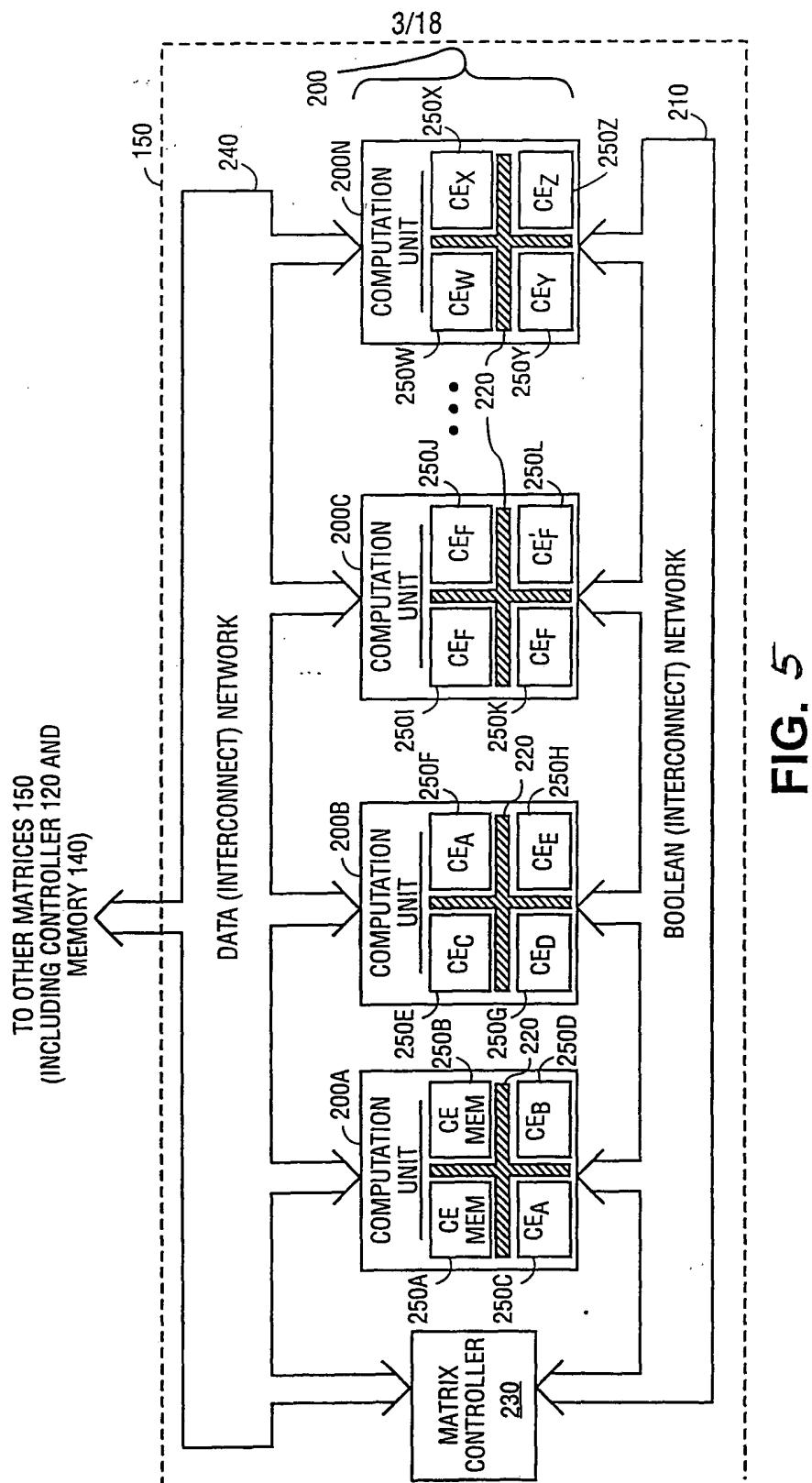


FIG. 5